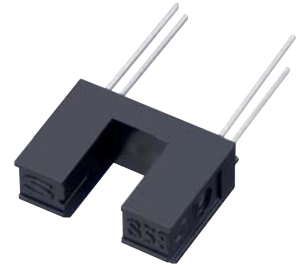


GP1S58VJ000F

Gap : 5mm, Slit : 0.5mm
Phototransistor Output,
Case package Transmissive
Photointerrupter



■ Description

GP1S58VJ000F is a standard, phototransistor output, transmissive photointerrupter with opposing emitter and detector in a case, providing non-contact sensing. For this family of devices, the emitter and detector are inserted in a case, resulting in a through-hole design.

This device uses positioning pins to insure accurate placement and avoid miss-orientation of the emitter and detector.

■ Features

1. Transmissive with phototransistor output
2. Highlights :
 - Vertical Slit for alternate motion detection
 - Positioning Pin to prevent misalignment
3. Key Parameters :
 - Gap Width : 5mm
 - Slit Width (detector side): 0.5mm
 - Package : 13.7×10×5.2mm
4. Lead free and RoHS directive compliant

■ Agency approvals/Compliance

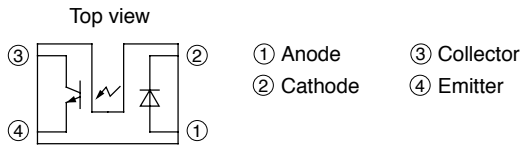
1. Compliant with RoHS directive

■ Applications

1. General purpose detection of object presence or motion.
2. Example : Printer, FAX, Optical storage unit

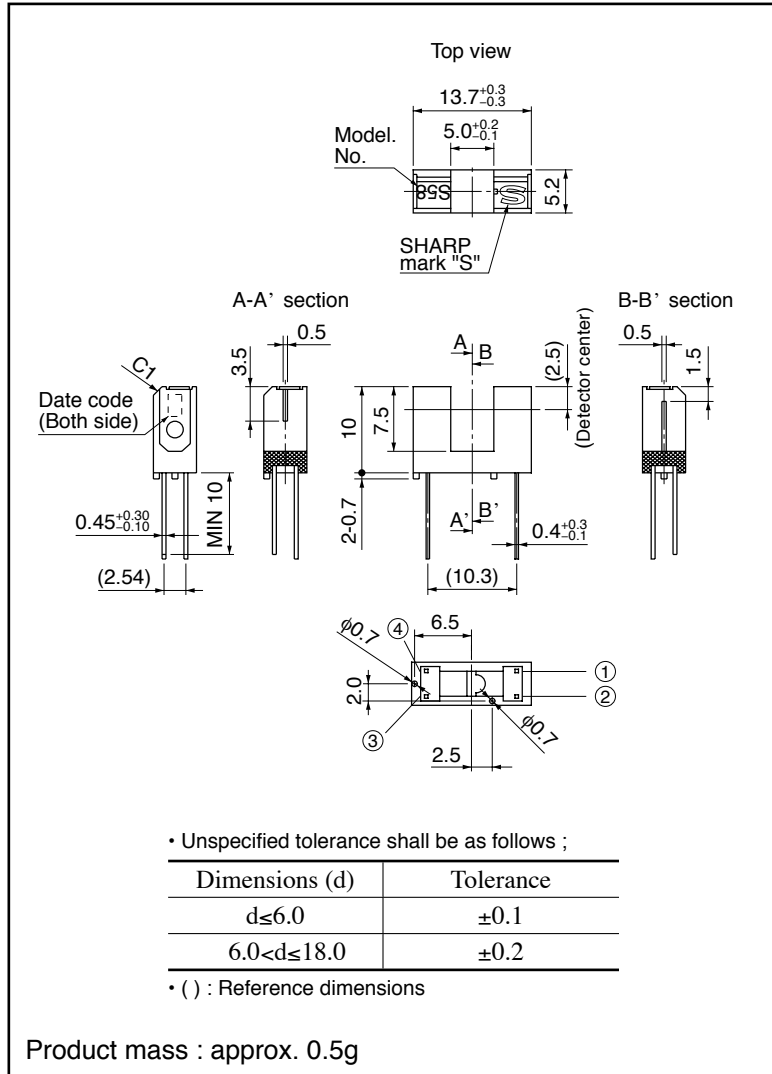
Notice The content of data sheet is subject to change without prior notice.
In the absence of confirmation by device specification sheets, SHARP takes no responsibility for any defects that may occur in equipment using any SHARP devices shown in catalogs, data books, etc. Contact SHARP in order to obtain the latest device specification sheets before using any SHARP device.

Internal Connection Diagram



Outline Dimensions

(Unit : mm)



Dip soldering material : Sn-3Ag-0.5Cu

Date code (2 digit)

1st digit		2nd digit	
Year of production		Month of production	
A.D.	Mark	Month	Mark
2000	0	1	1
2001	1	2	2
2002	2	3	3
2003	3	4	4
2004	4	5	5
2005	5	6	6
2006	6	7	7
2007	7	8	8
2008	8	9	9
2009	9	10	X
2010	0	11	Y
:	:	12	Z

repeats in a 10 year cycle

Country of origin

Japan, Indonesia or Philippines
(Indicated on the packing case)

■ Absolute Maximum Ratings (T_a=25°C)

Parameter		Symbol	Rating	Unit
Input	*1 Forward current	I _F	50	mA
	*1, 2 Peak forward current	I _{FM}	1	A
	Reverse voltage	V _R	6	V
	Power dissipation	P	75	mW
Output	Collector-emitter voltage	V _{CEO}	35	V
	Emitter-collector voltage	V _{ECO}	6	V
	Collector current	I _C	20	mA
	*1 Collector power dissipation	P _C	75	mW
Operating temperature		T _{opr}	-25 to +85	°C
Storage temperature		T _{stg}	-40 to +100	°C
*3 Soldering temperature		T _{sol}	260	°C

*1 Refer to Fig. 1, 2, 3

*2 Pulse width ≤ 100μs, Duty ratio=0.01

*3 For 5s or less

■ Electro-optical Characteristics (T_a=25°C)

Parameter		Symbol	Condition	MIN.	TYP.	MAX.	Unit
Input	Forward voltage	V _F	I _F =20mA	-	1.25	1.4	V
	Peak forward voltage	V _{FM}	I _{FM} =0.5A	-	3	4	V
	Reverse current	I _R	V _R =3V	-	-	10	μA
Output	Collector dark current	I _{CEO}	V _{CE} =20V	-	1	100	nA
Transfer characteristics	Collector current	I _C	V _{CE} =5V, I _F =20mA	0.5	-	15	mA
	Collector-emitter saturation voltage	V _{CE(sat)}	I _F =40mA, I _C =0.2mA	-	-	0.4	V
	Response time	Rise time	t _r	V _{CE} =2V, I _C =2mA, R _L =100Ω	-	3	15
Fall time		t _f	-		4	20	

Fig.1 Forward Current vs. Ambient Temperature

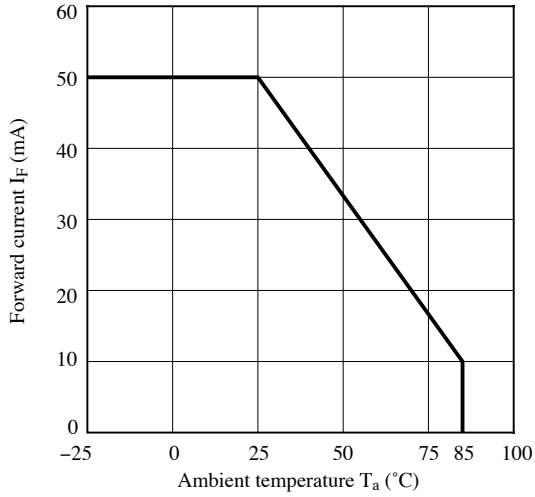


Fig.2 Collector Power Dissipation vs. Ambient Temperature

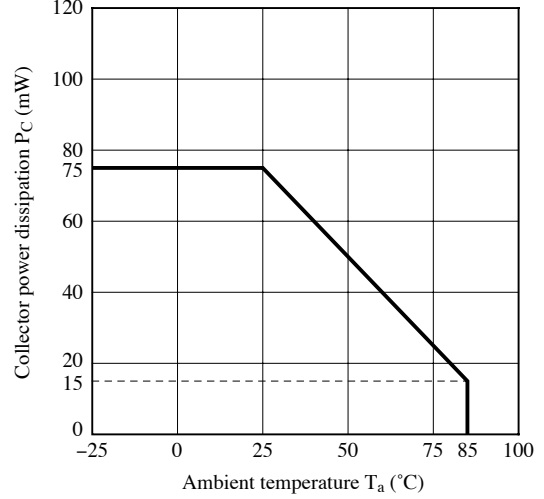


Fig.3 Peak Forward Current vs. Duty Ratio

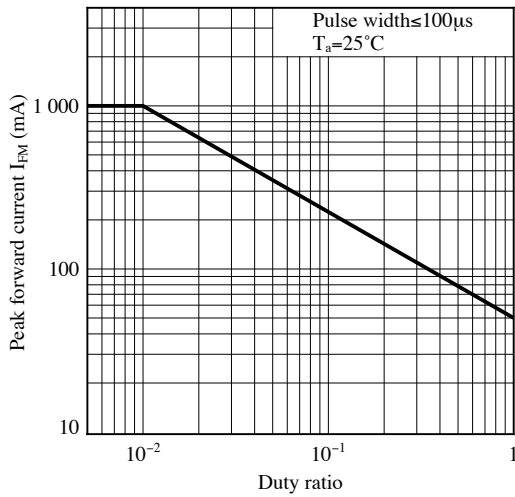


Fig.4 Forward Current vs. Forward Voltage

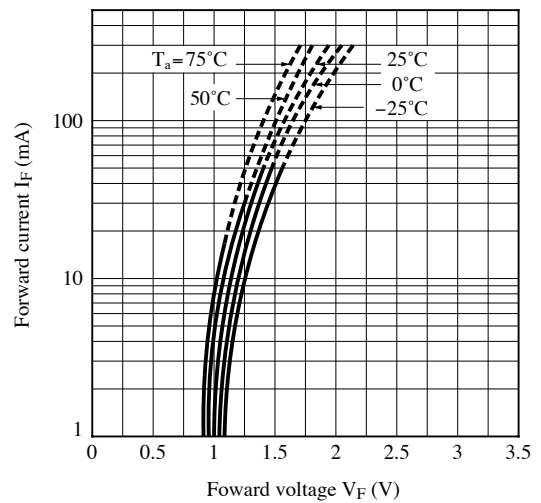


Fig.5 Collector Current vs. Forward Current

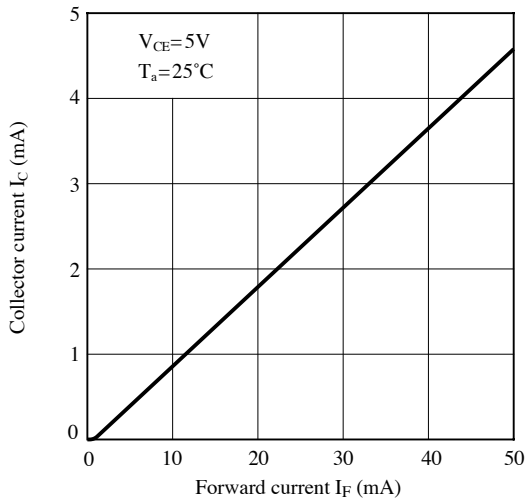


Fig.6 Collector Current vs. Collector-emitter Voltage

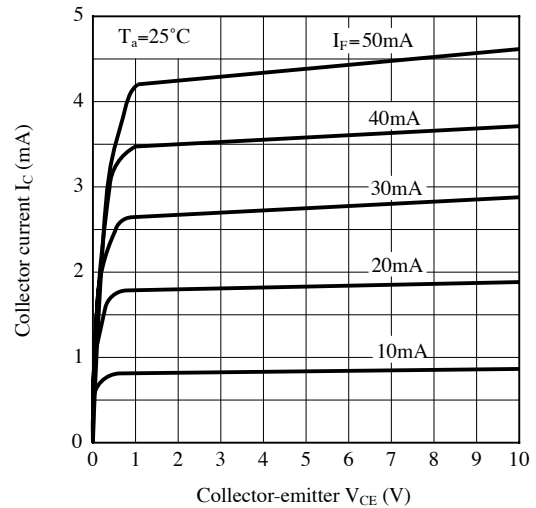


Fig.7 Collector Current vs. Ambient Temperature

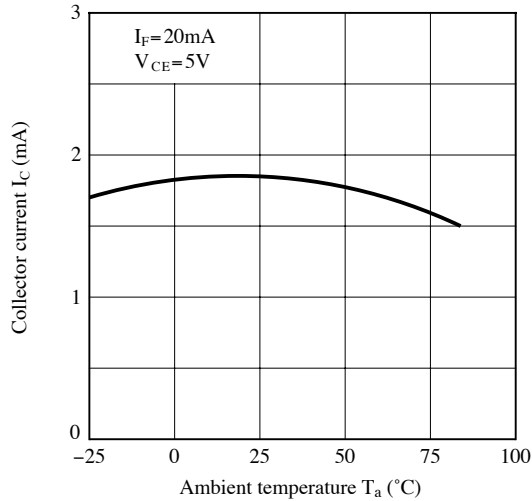


Fig.8 Collector-emitter Saturation Voltage vs. Ambient Temperature

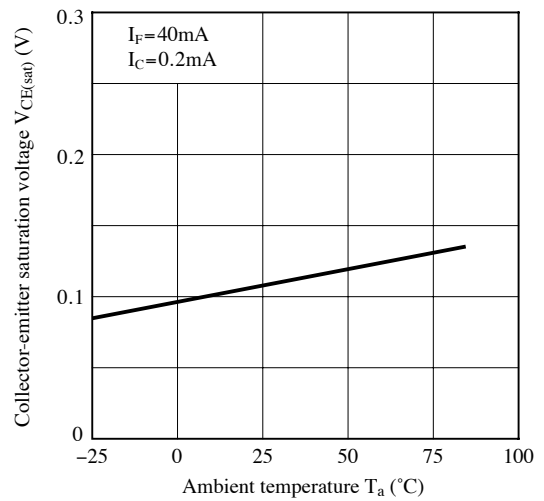


Fig.9 Response Time vs. Load Resistance

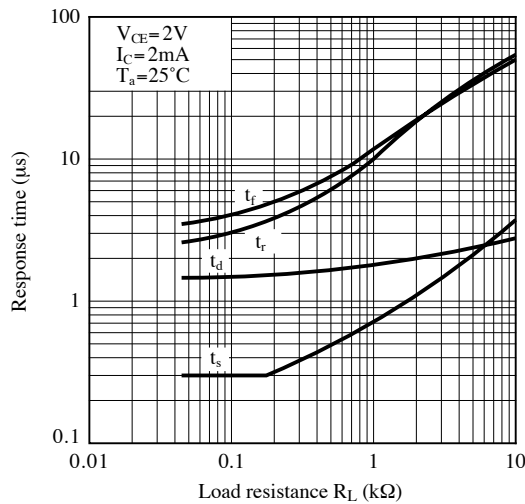


Fig.10 Test Circuit for Response Time

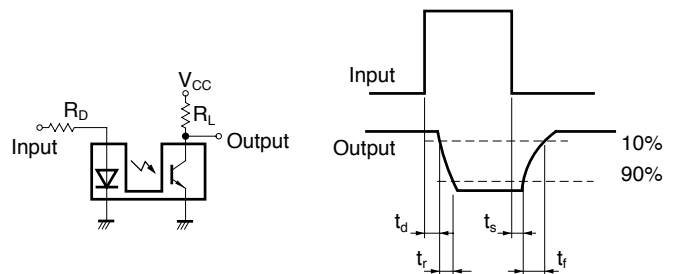


Fig.11 Frequency Response

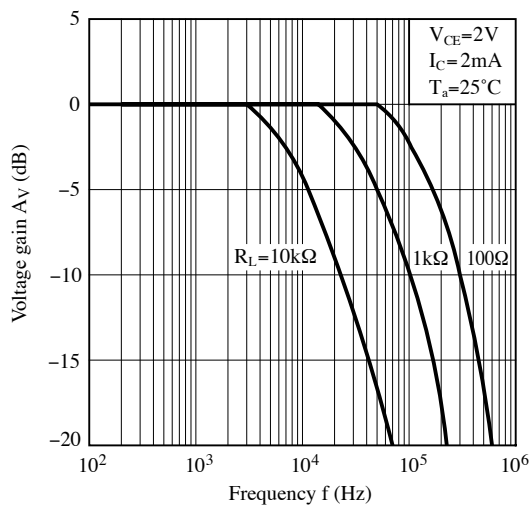


Fig.12 Collector Dark Current vs. Ambient Temperature

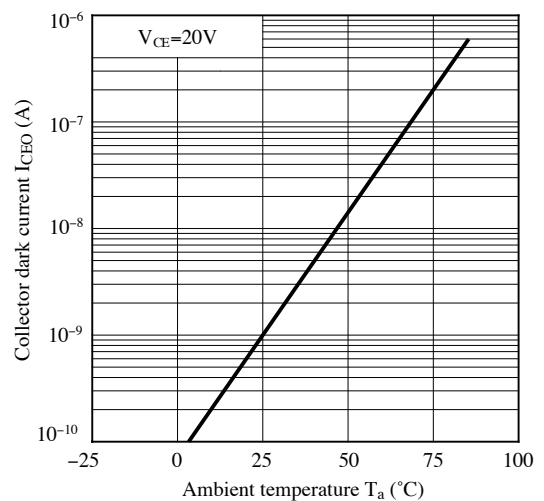
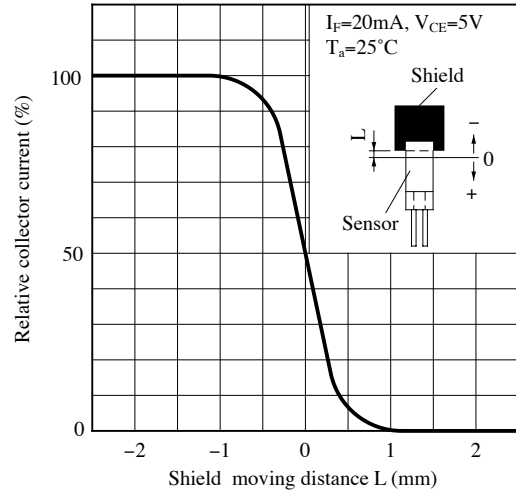
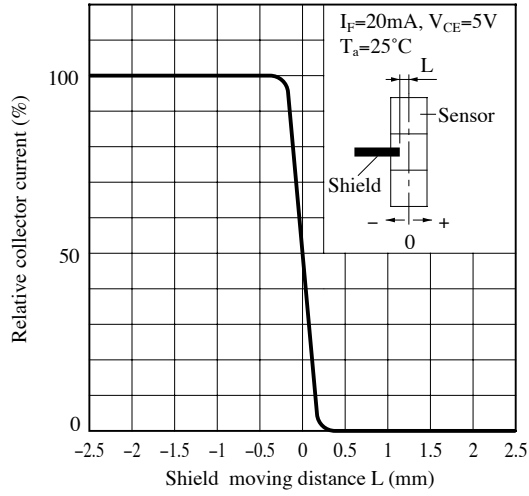


Fig.13 Detecting Position Characteristics (1)

Fig.14 Detecting Position Characteristics (2)



Remarks : Please be aware that all data in the graph are just for reference and not for guarantee.

■ **Design Considerations**

● **Design guide**

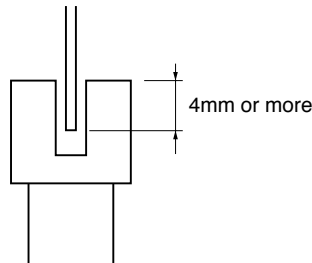
1) Prevention of detection error

To prevent photointerrupter from faulty operation caused by external light, do not set the detecting face to the external light.

2) Position of opaque board

Opaque board shall be installed at place 4mm or more from the top of elements.

(Example)



This product is not designed against irradiation and incorporates non-coherent IRED.

● **Degradation**

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5 years) into the design consideration.

● **Parts**

This product is assembled using the below parts.

• Photodetector (qty. : 1)

Category	Material	Maximum Sensitivity wavelength (nm)	Sensitivity wavelength (nm)	Response time (μs)
Phototransistor	Silicon (Si)	800	400 to 1 200	3

• Photo emitter (qty. : 1)

Category	Material	Maximum light emitting wavelength (nm)	I/O Frequency (MHz)
Infrared emitting diode (non-coherent)	Gallium arsenide (GaAs)	950	0.3

• **Material**

Case	Lead frame plating
Black NORYL resin	Solder dip. (Sn-3Ag-0.5Cu)

■ Manufacturing Guidelines**● Soldering Method****Flow Soldering:**

Soldering should be completed below 260°C and within 5 s.

Please take care not to let any external force exert on lead pins.

Please don't do soldering with preheating, and please don't do soldering by reflow.

Hand soldering

Hand soldering should be completed within 3 s when the point of solder iron is below 350°C.

Please solder within one time.

Please don't touch the terminals directly by soldering iron.

Soldered product shall treat at normal temperature.

Other notice

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the cooling and soldering conditions.

Flux

Some flux, which is used in soldering, may crack the package due to synergistic effect of alcohol in flux and the rise in temperature by heat in soldering. Therefore, in using flux, please make sure that it does not have any influence on appearance and reliability of the photointerrupter.

● Cleaning instructions**Solvent cleaning :**

Solvent temperature should be 45°C or below. Immersion time should be 3 minutes or less.

Ultrasonic cleaning :

The affect to device by ultrasonic cleaning is different by cleaning bath size, ultrasonic power output, cleaning time, PCB size or device mounting condition etc.

Please test it in actual using condition and confirm that doesn't occur any defect before starting the ultrasonic cleaning.

Recommended solvent materials :

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol.

● Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances : CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBB and PBDE are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive (2002/95/EC).

•Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).

■ Package specification**● Case package**

Package materials

Anti-static plastic bag : Polyethylene

Moltopren : Urethane

Partition : Corrugated fiberboard

Packing case : Corrugated fiberboard

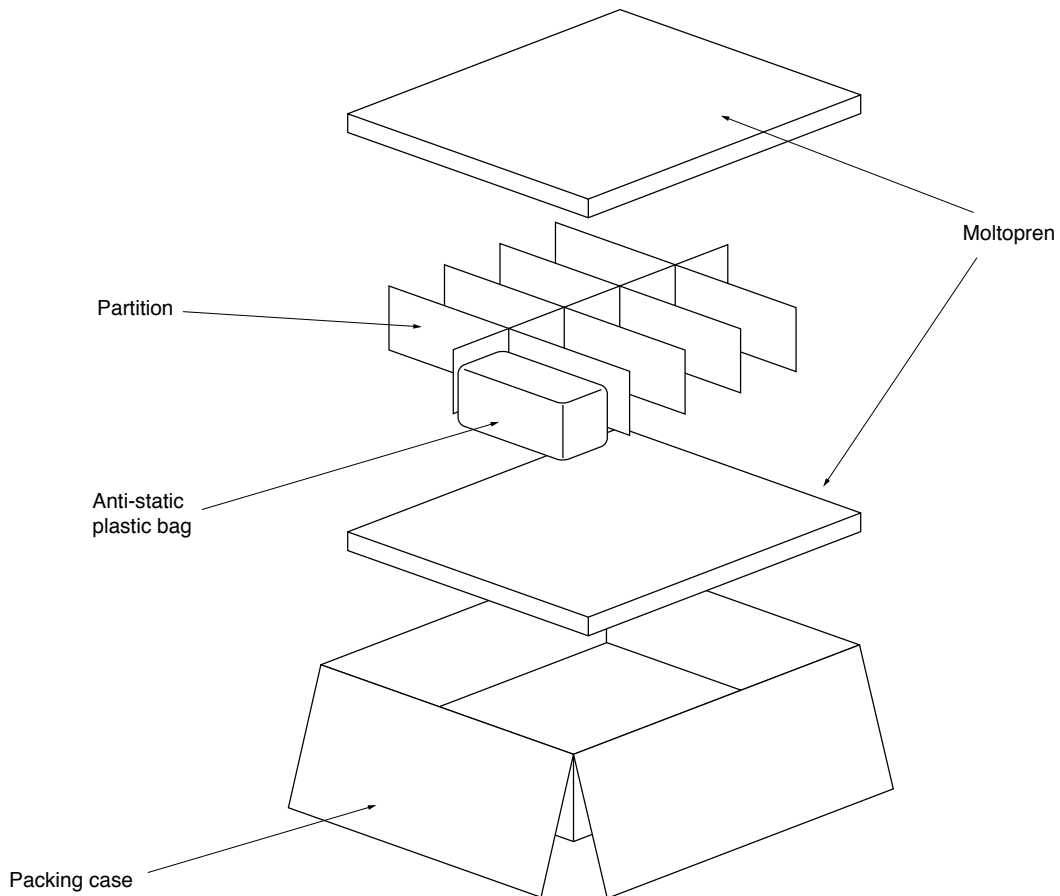
Package method

100 pcs of products shall be packaged in a plastic bag, Ends shall be fixed be by stoppers. The bottom of the packing case is covered with moltopren, and the partition is set in the packing case. Each partition should have 1 plastic bag.

The 10 plastic bags containing a product are put in the packing case.

Moltopren should be located after all product are settled (1 packing contains 1 000 pcs).

Packing composition



■ Important Notices

· The circuit application examples in this publication are provided to explain representative applications of SHARP devices and are not intended to guarantee any circuit design or license any intellectual property rights. SHARP takes no responsibility for any problems related to any intellectual property right of a third party resulting from the use of SHARP's devices.

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(i) The devices in this publication are designed for use in general electronic equipment designs such as:

- Personal computers
- Office automation equipment
- Telecommunication equipment [terminal]
- Test and measurement equipment
- Industrial control
- Audio visual equipment
- Consumer electronics

(ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

with equipment that requires higher reliability such as:

- Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
- Traffic signals
- Gas leakage sensor breakers
- Alarm equipment
- Various safety devices, etc.

(iii) SHARP devices shall not be used for or in connection with equipment that requires an extremely high level of reliability and safety such as:

- Space applications
- Telecommunication equipment [trunk lines]
- Nuclear power control equipment
- Medical and other life support equipment (e.g., scuba).

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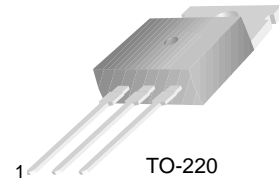
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TIP120/121/122

Medium Power Linear Switching Applications

- Complementary to TIP125/126/127



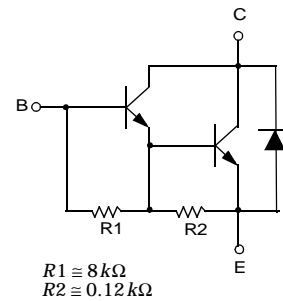
TO-220
1.Base 2.Collector 3.Emitter

NPN Epitaxial Darlington Transistor

Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage : TIP120	60	V
	: TIP121	80	V
	: TIP122	100	V
V_{CEO}	Collector-Emitter Voltage : TIP120	60	V
	: TIP121	80	V
	: TIP122	100	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current (DC)	5	A
I_{CP}	Collector Current (Pulse)	8	A
I_B	Base Current (DC)	120	mA
P_C	Collector Dissipation ($T_a=25^\circ\text{C}$)	2	W
	Collector Dissipation ($T_C=25^\circ\text{C}$)	65	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature	- 65 ~ 150	$^\circ\text{C}$

Equivalent Circuit



Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
$V_{CEO(sus)}$	Collector-Emitter Sustaining Voltage	$I_C = 100\text{mA}, I_B = 0$	60		V
	: TIP120				
	: TIP121				
I_{CEO}	Collector Cut-off Current	$V_{CE} = 30\text{V}, I_B = 0$ $V_{CE} = 40\text{V}, I_B = 0$ $V_{CE} = 50\text{V}, I_B = 0$		0.5	mA
	: TIP120				
	: TIP121				
I_{CBO}	Collector Cut-off Current	$V_{CB} = 60\text{V}, I_E = 0$ $V_{CB} = 80\text{V}, I_E = 0$ $V_{CB} = 100\text{V}, I_E = 0$		0.2	mA
	: TIP120				
	: TIP121				
I_{EBO}	Emitter Cut-off Current	$V_{BE} = 5\text{V}, I_C = 0$		2	mA
h_{FE}	* DC Current Gain	$V_{CE} = 3\text{V}, I_C = 0.5\text{A}$ $V_{CE} = 3\text{V}, I_C = 3\text{A}$	1000		
$V_{CE(sat)}$	* Collector-Emitter Saturation Voltage	$I_C = 3\text{A}, I_B = 12\text{mA}$ $I_C = 5\text{A}, I_B = 20\text{mA}$		2.0	V
				4.0	V
$V_{BE(on)}$	* Base-Emitter ON Voltage	$V_{CE} = 3\text{V}, I_C = 3\text{A}$		2.5	V
C_{ob}	Output Capacitance	$V_{CB} = 10\text{V}, I_E = 0, f = 0.1\text{MHz}$		200	pF

* Pulse Test : $PW \leq 300\mu\text{s}$, Duty cycle $\leq 2\%$

Typical characteristics

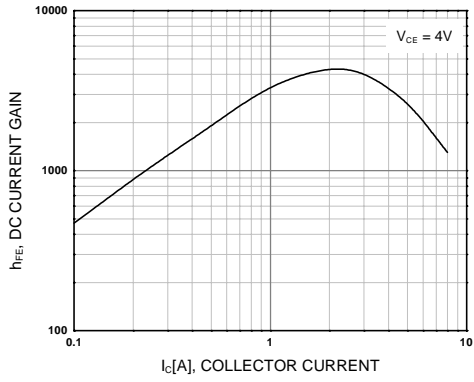


Figure 1. DC current Gain

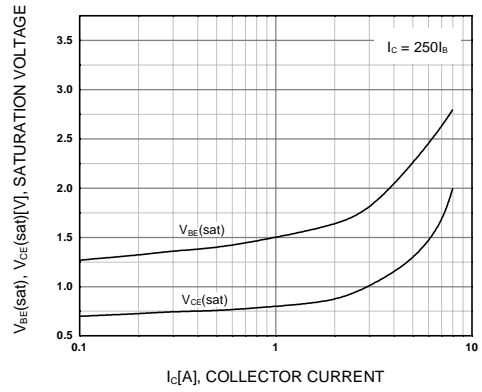


Figure 2. Base-Emitter Saturation Voltage
Collector-Emitter Saturation Voltage

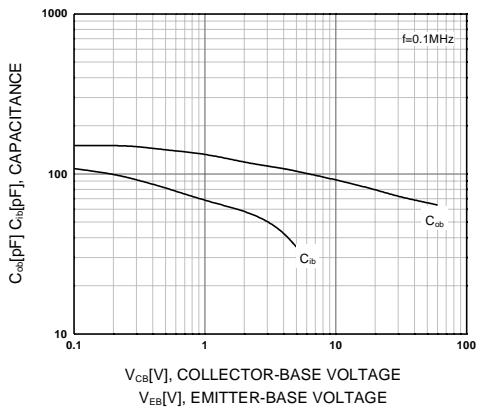


Figure 3. Output and Input Capacitance
vs. Reverse Voltage

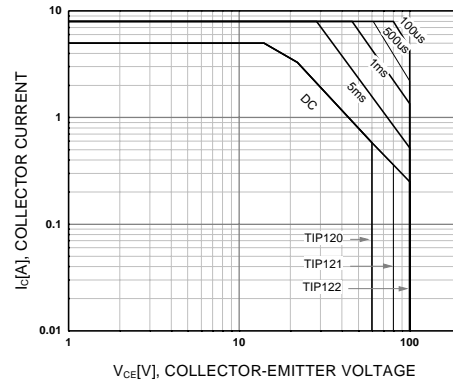


Figure 4. Safe Operating Area

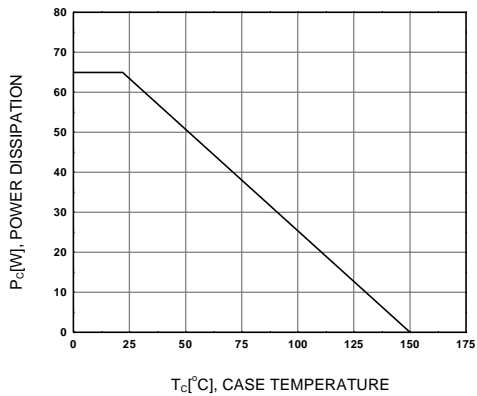
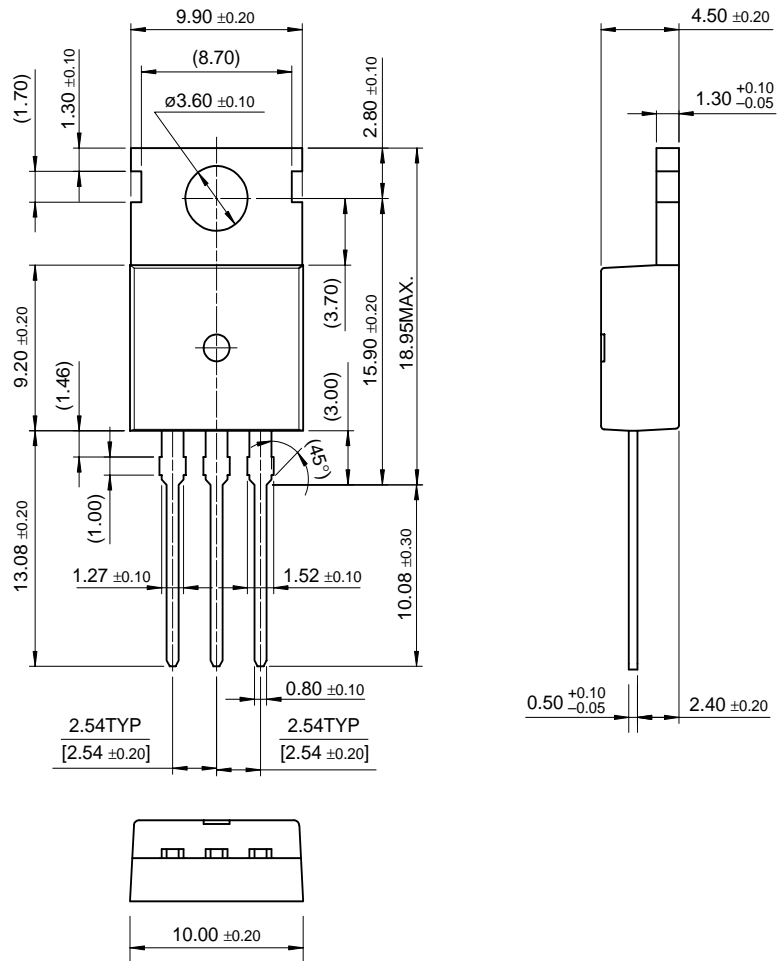


Figure 5. Power Derating

Package Dimensions

TO-220

TIP120/121/122



Dimensions in Millimeters

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PRODUCT STATUS DEFINITIONS

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Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.



ULN2001A-ULN2002A ULN2003A-ULN2004A

SEVEN DARLINGTON ARRAYS

- SEVEN DARLINGTONS PER PACKAGE
- OUTPUT CURRENT 500mA PER DRIVER (600mA PEAK)
- OUTPUT VOLTAGE 50V
- INTEGRATED SUPPRESSION DIODES FOR INDUCTIVE LOADS
- OUTPUTS CAN BE PARALLELED FOR HIGHER CURRENT
- TTL/CMOS/PMOS/DTL COMPATIBLE INPUTS
- INPUTS PINNED OPPOSITE OUTPUTS TO SIMPLIFY LAYOUT

DESCRIPTION

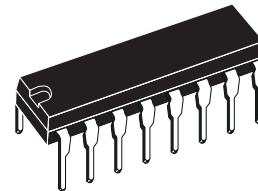
The ULN2001A, ULN2002A, ULN2003 and ULN2004A are high voltage, high current darlington arrays each containing seven open collector darlington pairs with common emitters. Each channel rated at 500mA and can withstand peak currents of 600mA. Suppression diodes are included for inductive load driving and the inputs are pinned opposite the outputs to simplify board layout.

The four versions interface to all common logic families :

ULN2001A	General Purpose, DTL, TTL, PMOS, CMOS
ULN2002A	14-25V PMOS
ULN2003A	5V TTL, CMOS
ULN2004A	6-15V CMOS, PMOS

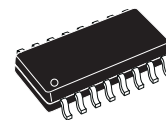
These versatile devices are useful for driving a wide range of loads including solenoids, relays DC motors, LED displays filament lamps, thermal print-heads and high power buffers.

The ULN2001A/2002A/2003A and 2004A are supplied in 16 pin plastic DIP packages with a copper leadframe to reduce thermal resistance. They are available also in small outline package (SO-16) as ULN2001D/2002D/2003D/2004D.



DIP16

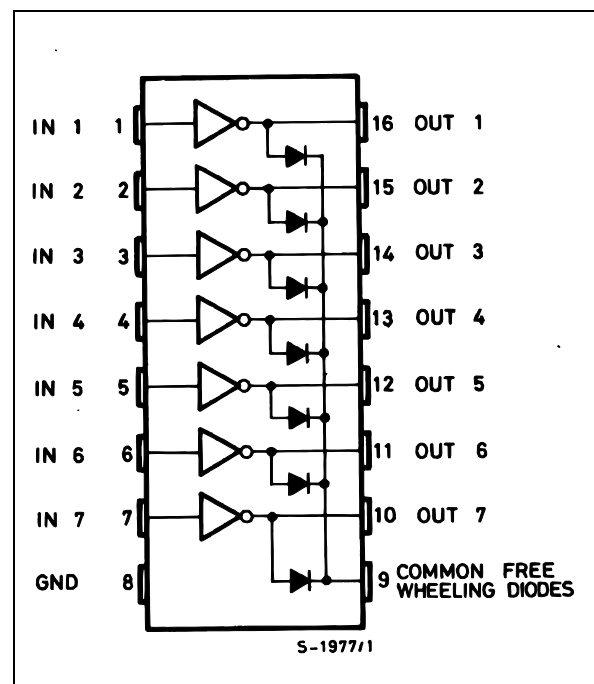
ORDERING NUMBERS: ULN2001A/2A/3A/4A



SO16

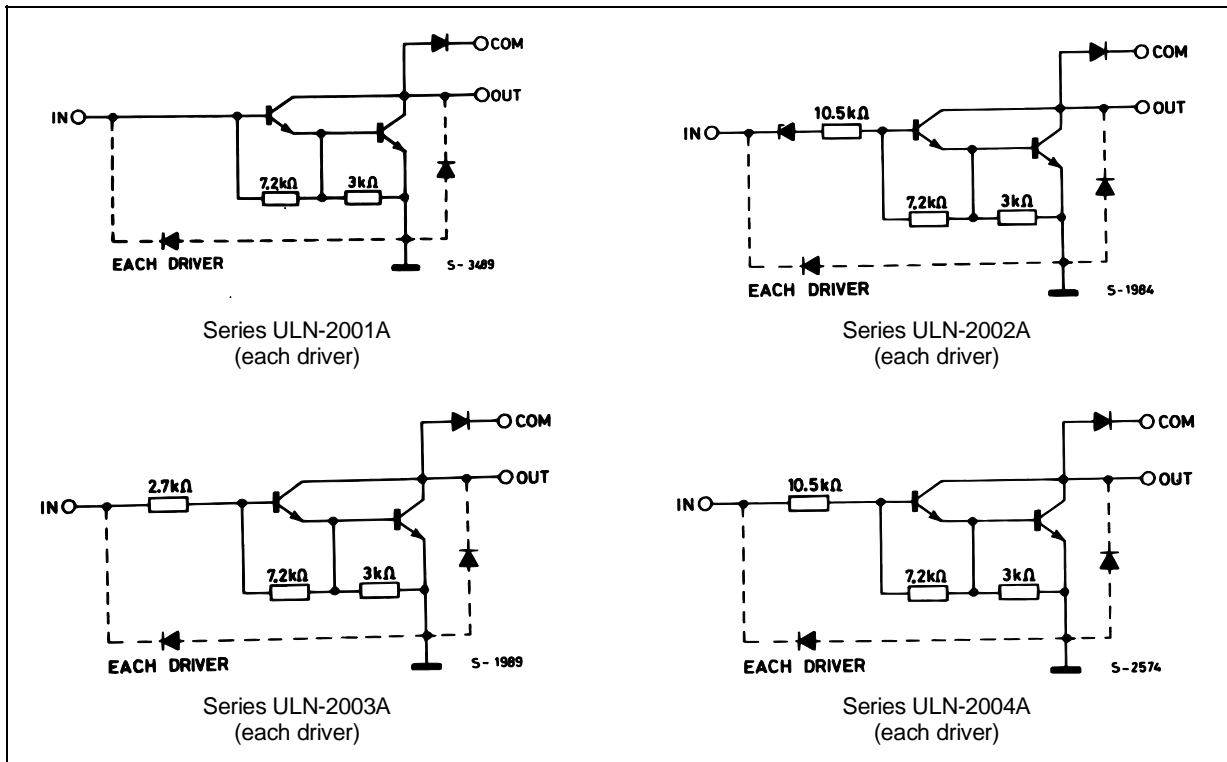
ORDERING NUMBERS: ULN2001D/2D/3D/4D

PIN CONNECTION



ULN2001A - ULN2002A - ULN2003A - ULN2004A

SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_o	Output Voltage	50	V
V_{in}	Input Voltage (for ULN2002A/D - 2003A/D - 2004A/D)	30	V
I_c	Continuous Collector Current	500	mA
I_b	Continuous Base Current	25	mA
T_{amb}	Operating Ambient Temperature Range	- 20 to 85	°C
T_{stg}	Storage Temperature Range	- 55 to 150	°C
T_j	Junction Temperature	150	°C

THERMAL DATA

Symbol	Parameter	DIP16	SO16	Unit
$R_{th j-amb}$	Thermal Resistance Junction-ambient	Max. 70	120	°C/W

ULN2001A - ULN2002A - ULN2003A - ULN2004A

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit	Fig.
I_{CEX}	Output Leakage Current	$V_{CE} = 50\text{V}$ $T_{amb} = 70^{\circ}\text{C}, V_{CE} = 50\text{V}$			50 100	μA μA	1a 1a
		$T_{amb} = 70^{\circ}\text{C}$ for ULN2002A $V_{CE} = 50\text{V}, V_i = 6\text{V}$			500	μA	1b
		for ULN2004A $V_{CE} = 50\text{V}, V_i = 1\text{V}$			500	μA	1b
$V_{CE(sat)}$	Collector-emitter Saturation Voltage	$I_C = 100\text{mA}, I_B = 250\mu\text{A}$		0.9	1.1	V	2
		$I_C = 200\text{mA}, I_B = 350\mu\text{A}$		1.1	1.3	V	2
		$I_C = 350\text{mA}, I_B = 500\mu\text{A}$		1.3	1.6	V	2
$I_{i(on)}$	Input Current	for ULN2002A, $V_i = 17\text{V}$		0.82	1.25	mA	3
		for ULN2003A, $V_i = 3.85\text{V}$		0.93	1.35	mA	3
		for ULN2004A, $V_i = 5\text{V}$		0.35	0.5	mA	3
		$V_i = 12\text{V}$		1	1.45	mA	3
$I_{i(off)}$	Input Current	$T_{amb} = 70^{\circ}\text{C}, I_C = 500\mu\text{A}$	50	65		μA	4
$V_{i(on)}$	Input Voltage	$V_{CE} = 2\text{V}$ for ULN2002A $I_C = 300\text{mA}$			13	V	5
		for ULN2003A $I_C = 200\text{mA}$			2.4		
		$I_C = 250\text{mA}$			2.7		
		$I_C = 300\text{mA}$			3		
		for ULN2004A $I_C = 125\text{mA}$			5		
		$I_C = 200\text{mA}$			6		
		$I_C = 275\text{mA}$			7		
		$I_C = 350\text{mA}$			8		
h_{FE}	DC Forward Current Gain	for ULN2001A $V_{CE} = 2\text{V}, I_C = 350\text{mA}$	1000				2
C_i	Input Capacitance			15	25	pF	
t_{PLH}	Turn-on Delay Time	$0.5 V_i$ to $0.5 V_o$		0.25	1	μs	
t_{PHL}	Turn-off Delay Time	$0.5 V_i$ to $0.5 V_o$		0.25	1	μs	
I_R	Clamp Diode Leakage Current	$V_R = 50\text{V}$			50	μA	6
		$T_{amb} = 70^{\circ}\text{C}, V_R = 50\text{V}$			100	μA	6
V_F	Clamp Diode Forward Voltage	$I_F = 350\text{mA}$		1.7	2	V	7

TEST CIRCUITS

Figure 1a.

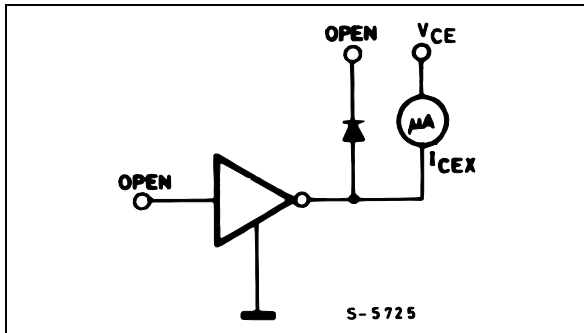


Figure 1b.

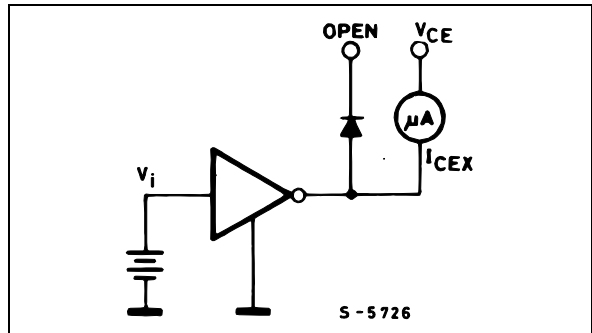


Figure 2.

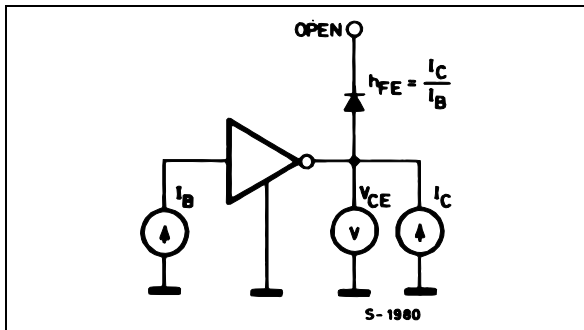


Figure 3.

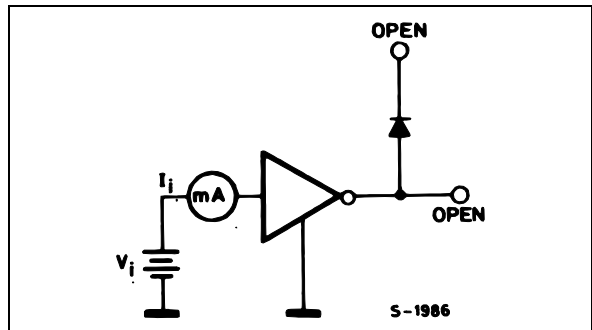


Figure 4.

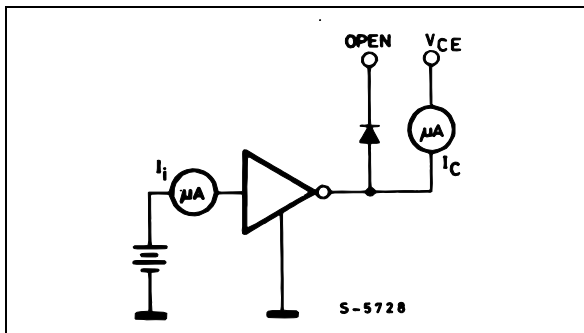


Figure 5.

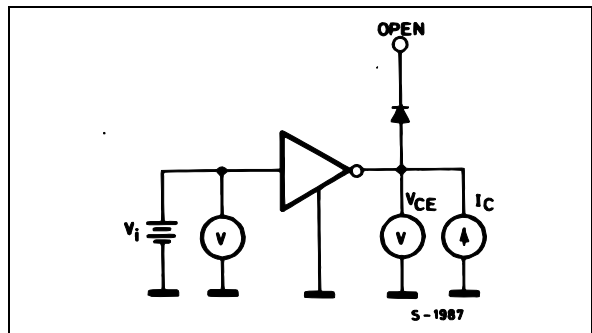


Figure 6.

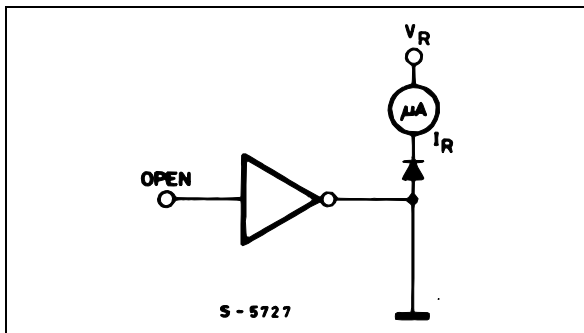


Figure 7.

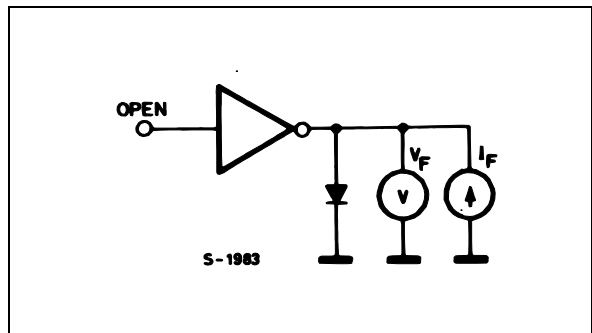


Figure 8: Collector Current versus Input Current

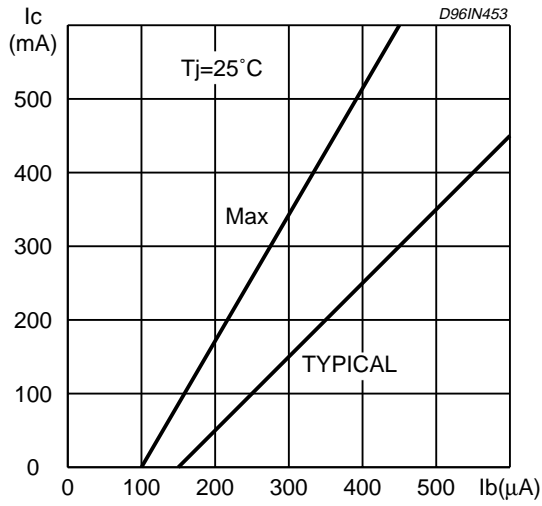


Figure 9: Collector Current versus Saturation Voltage

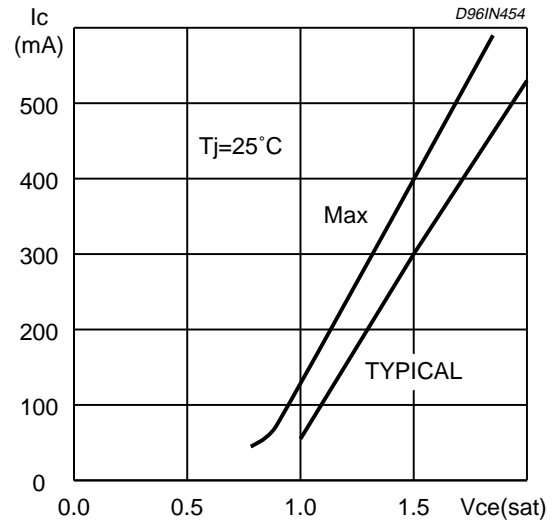


Figure 10: Peak Collector Current versus Duty Cycle

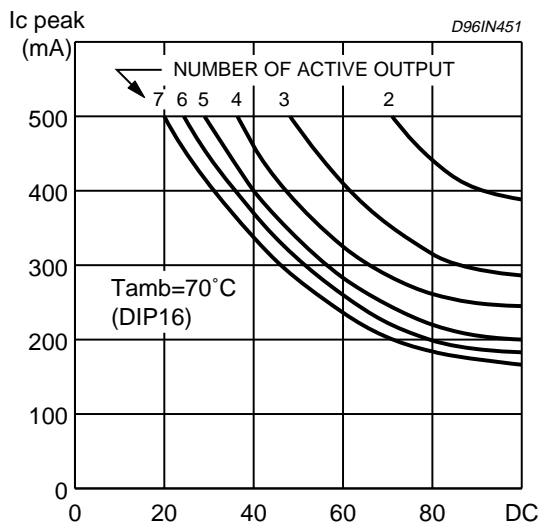
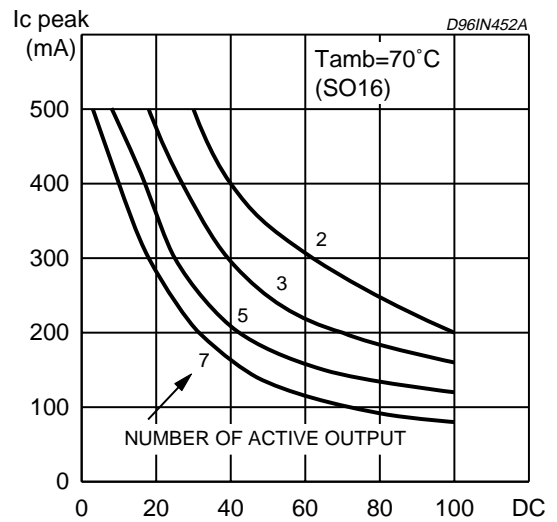


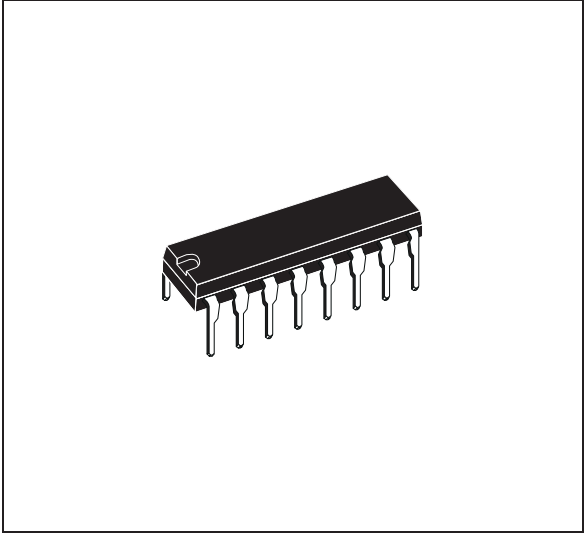
Figure 11: Peak Collector Current versus Duty Cycle



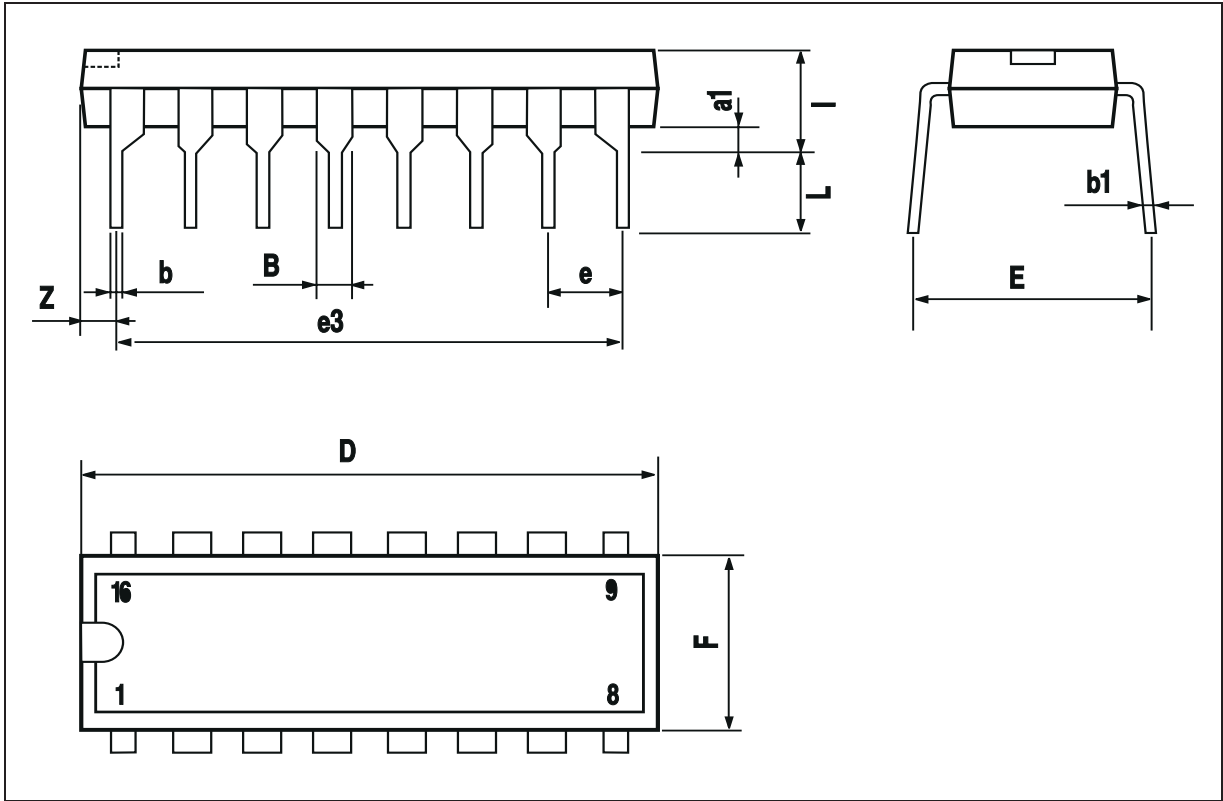
ULN2001A - ULN2002A - ULN2003A - ULN2004A

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a1	0.51			0.020		
B	0.77		1.65	0.030		0.065
b		0.5			0.020	
b1		0.25			0.010	
D			20			0.787
E		8.5			0.335	
e		2.54			0.100	
e3		17.78			0.700	
F			7.1			0.280
I			5.1			0.201
L		3.3			0.130	
Z			1.27			0.050

OUTLINE AND MECHANICAL DATA

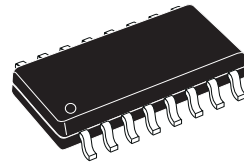


DIP16



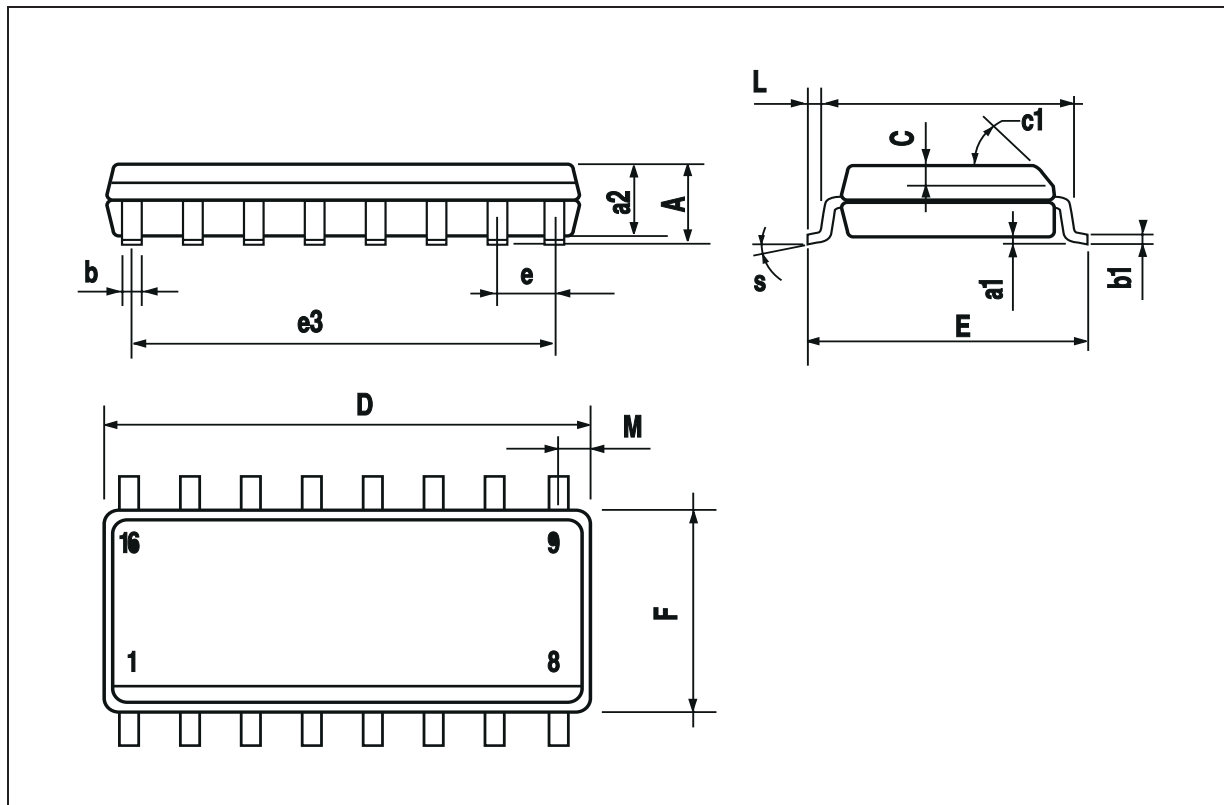
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			1.75			0.069
a1	0.1		0.25	0.004		0.009
a2			1.6			0.063
b	0.35		0.46	0.014		0.018
b1	0.19		0.25	0.007		0.010
C		0.5			0.020	
c1	45° (typ.)					
D (1)	9.8		10	0.386		0.394
E	5.8		6.2	0.228		0.244
e		1.27			0.050	
e3		8.89			0.350	
F (1)	3.8		4	0.150		0.157
G	4.6		5.3	0.181		0.209
L	0.4		1.27	0.016		0.050
M			0.62			0.024
S	8°(max.)					

OUTLINE AND MECHANICAL DATA



SO16 Narrow

(1) D and F do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm (.006inch).



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