## Design of a fault-finding device

by : Koen BROEKX Xavier FONTANET Molinero

promotors :	Mr. W. CLAES - KHLIM
	Miss. M. DAENEN - KHLIM
	Miss. K. BESSEMANS - lecturer

We did our final project at KHLim in Diepenbeek during a period of 12 weeks. The assignment was given by W. Claes, a lecturer at this school.

Our mission was to design a total new fault-finding device for relay motor circuits, which had to replace the current one because of security reasons. This device is being used in the electricity lab.

We had to do this by using all of the latest techniques. First we studied the existing device by simulating all of the tests. After that we ordered all the components we would need to build the new simulator. Meanwhile we started drawing the mechanical design with AutoCAD. When the components arrived we build our simulator. The electrical design of the device was drawn with Eplan. Then we wrote the program for the S7 PLC. We also programmed the touchpanel which is being used to choose the circuits and the errors. We simulated all of the tests and checked if everything was working fine.

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